

DRAWINGS (See Visio Attachment)

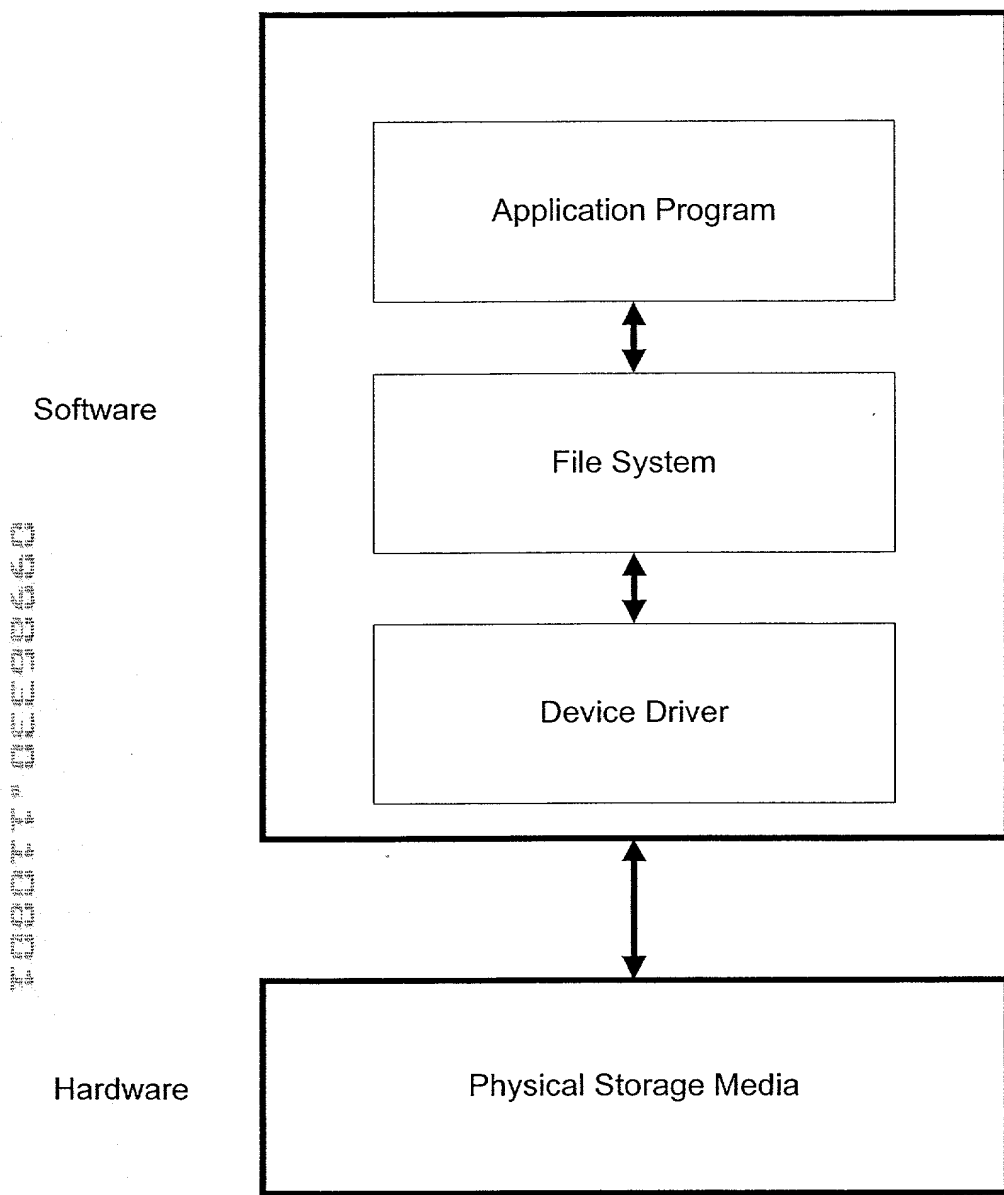


FIGURE 1

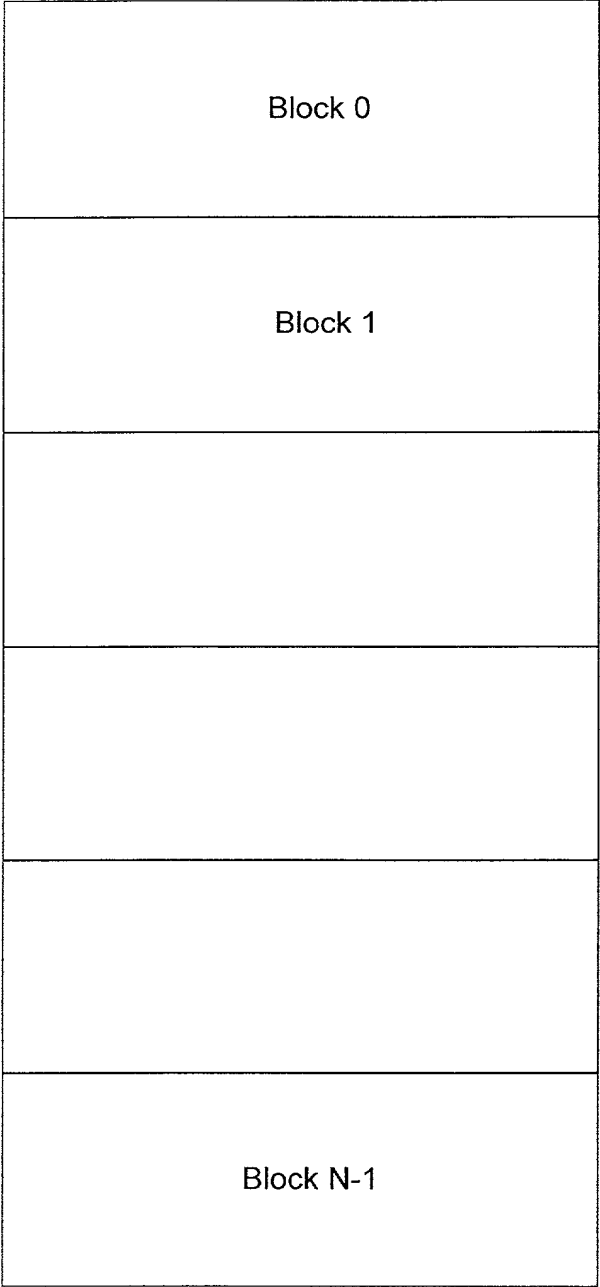
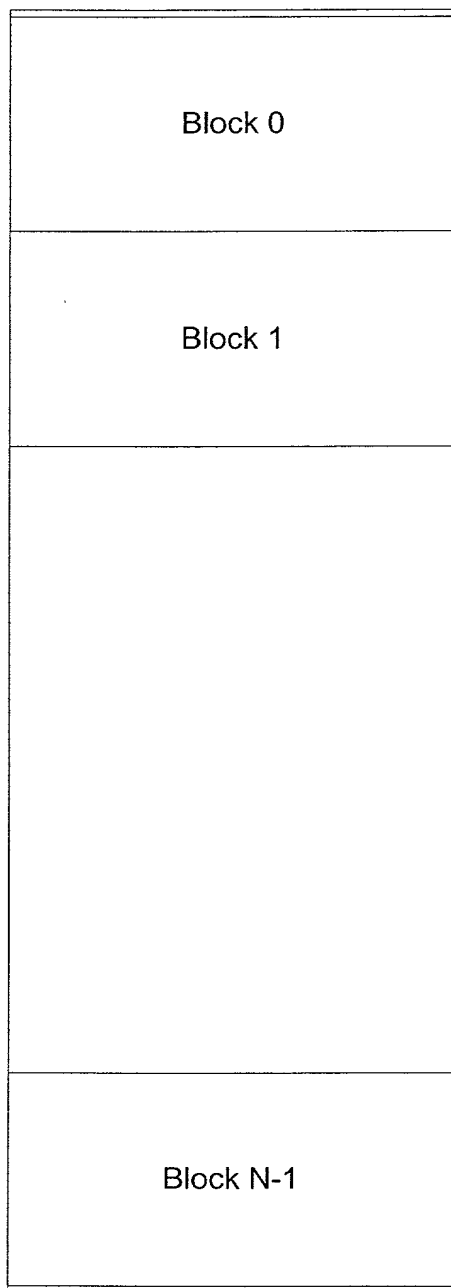


FIGURE 2

User
View

Virtual Blocks



Physical Blocks

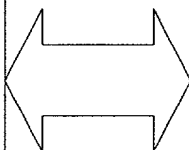
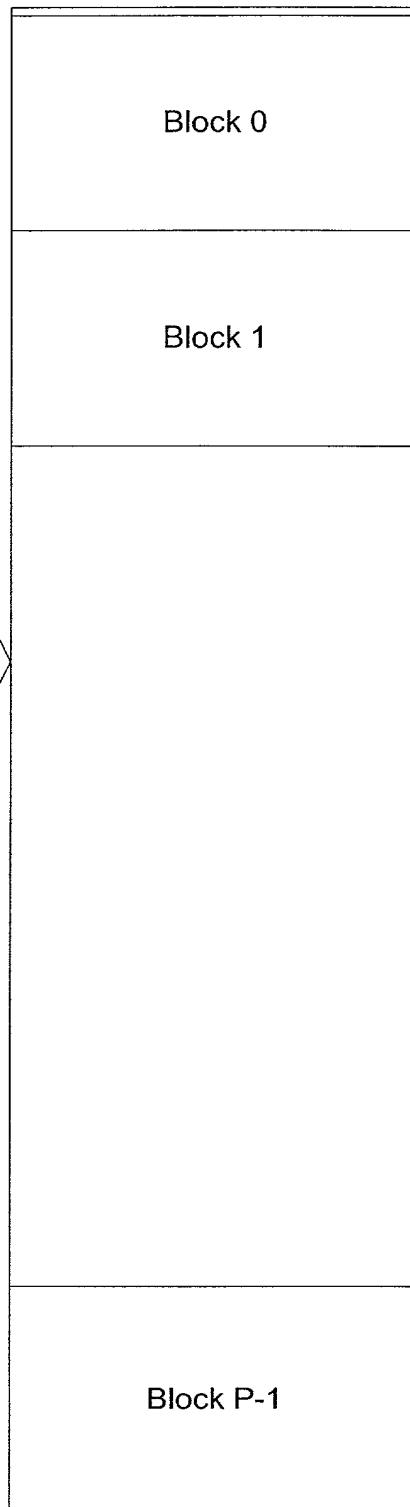


FIGURE 3

U.S. Patent and Trademark Office

User
View

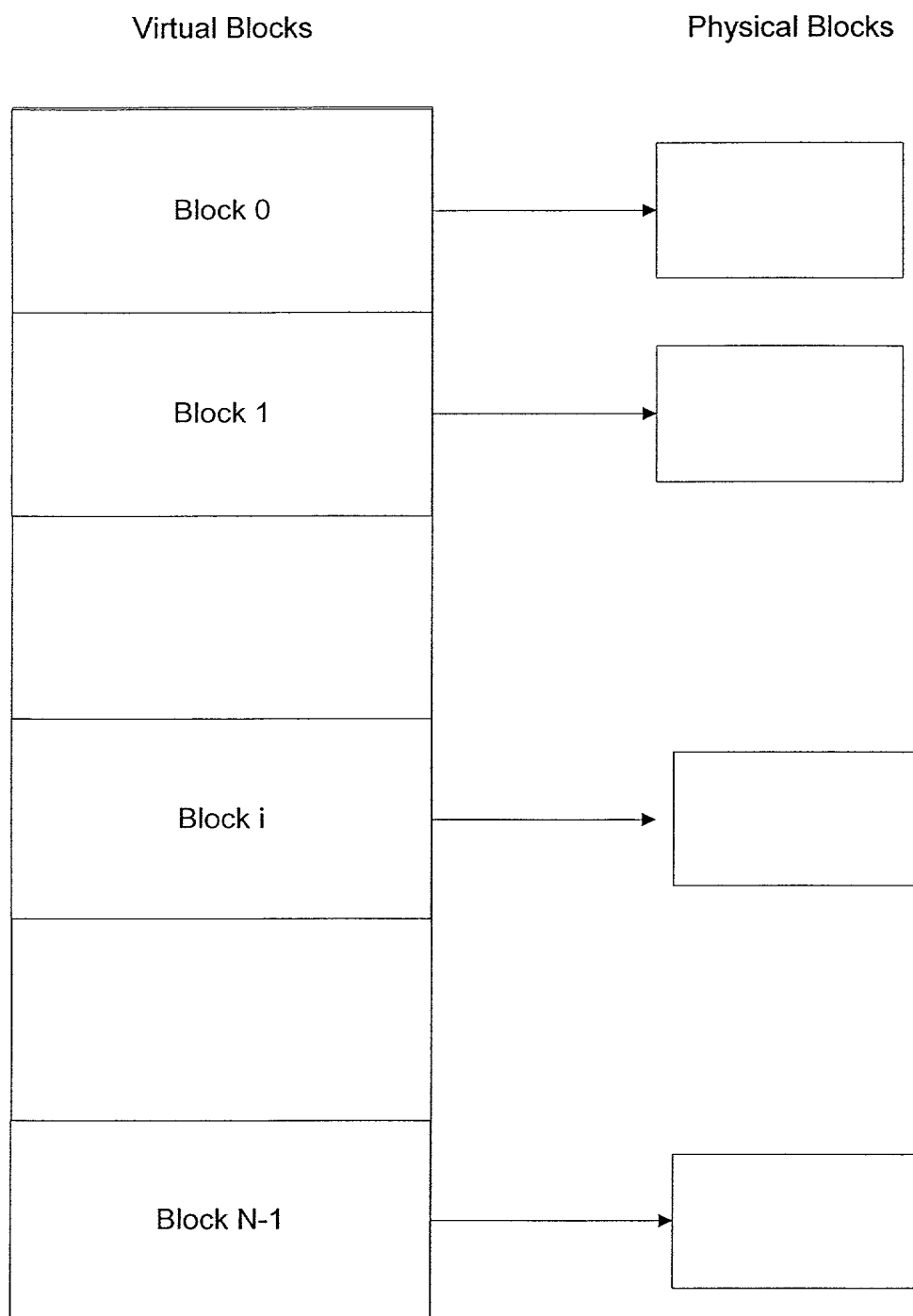


FIGURE 4A

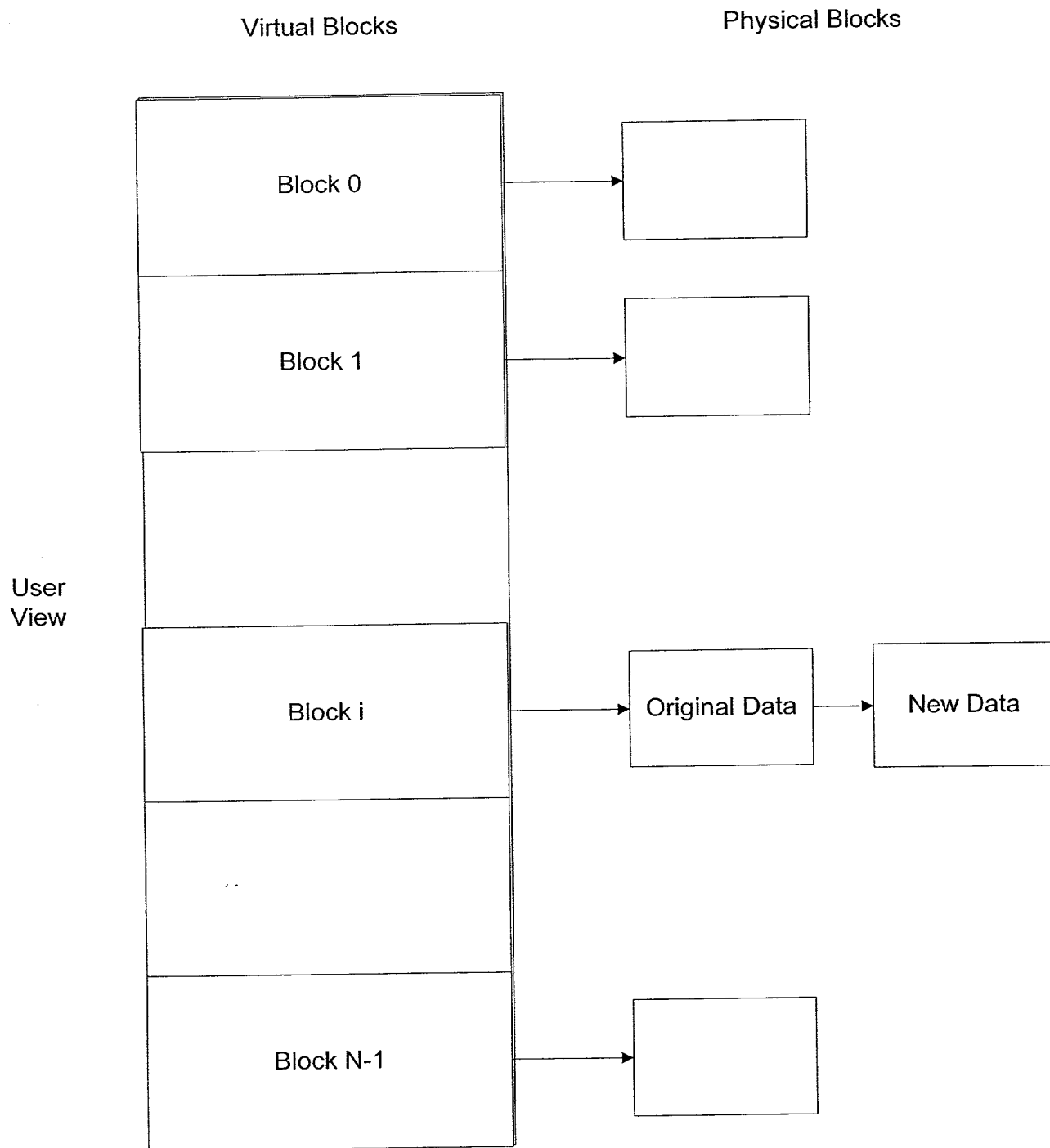


FIGURE 4B

User
View

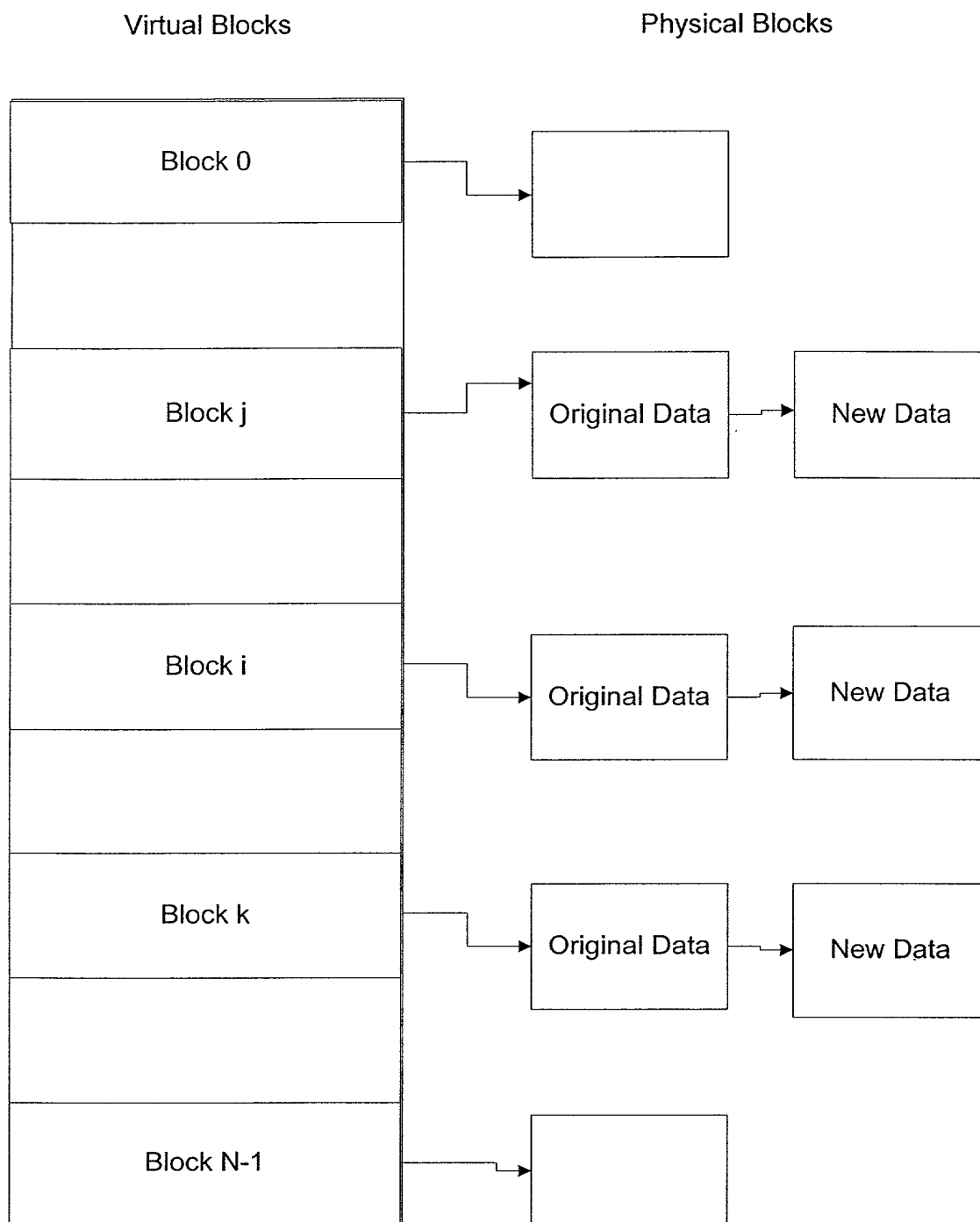


FIGURE 4C

FIGURE 4D

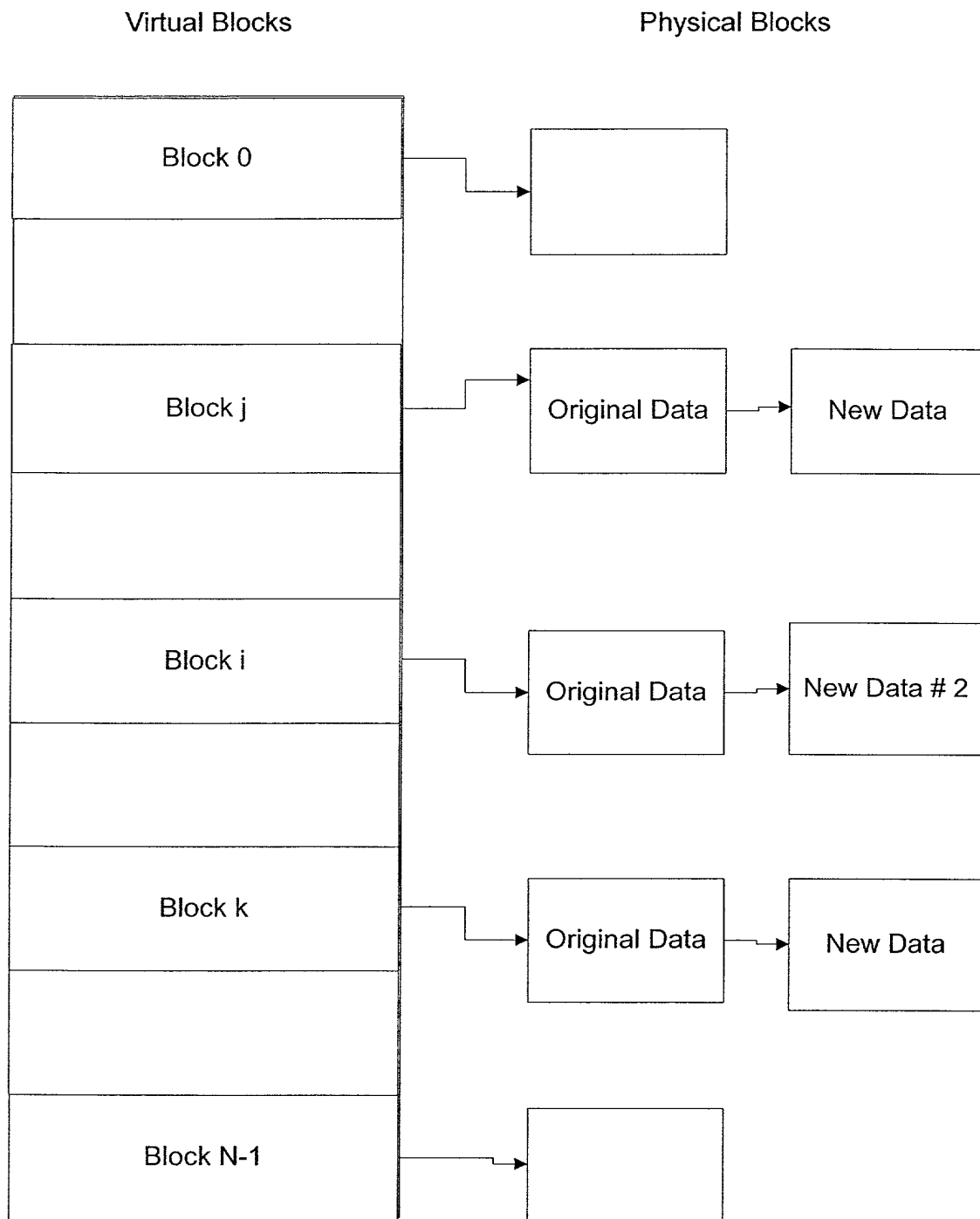


FIGURE 4D

User
View

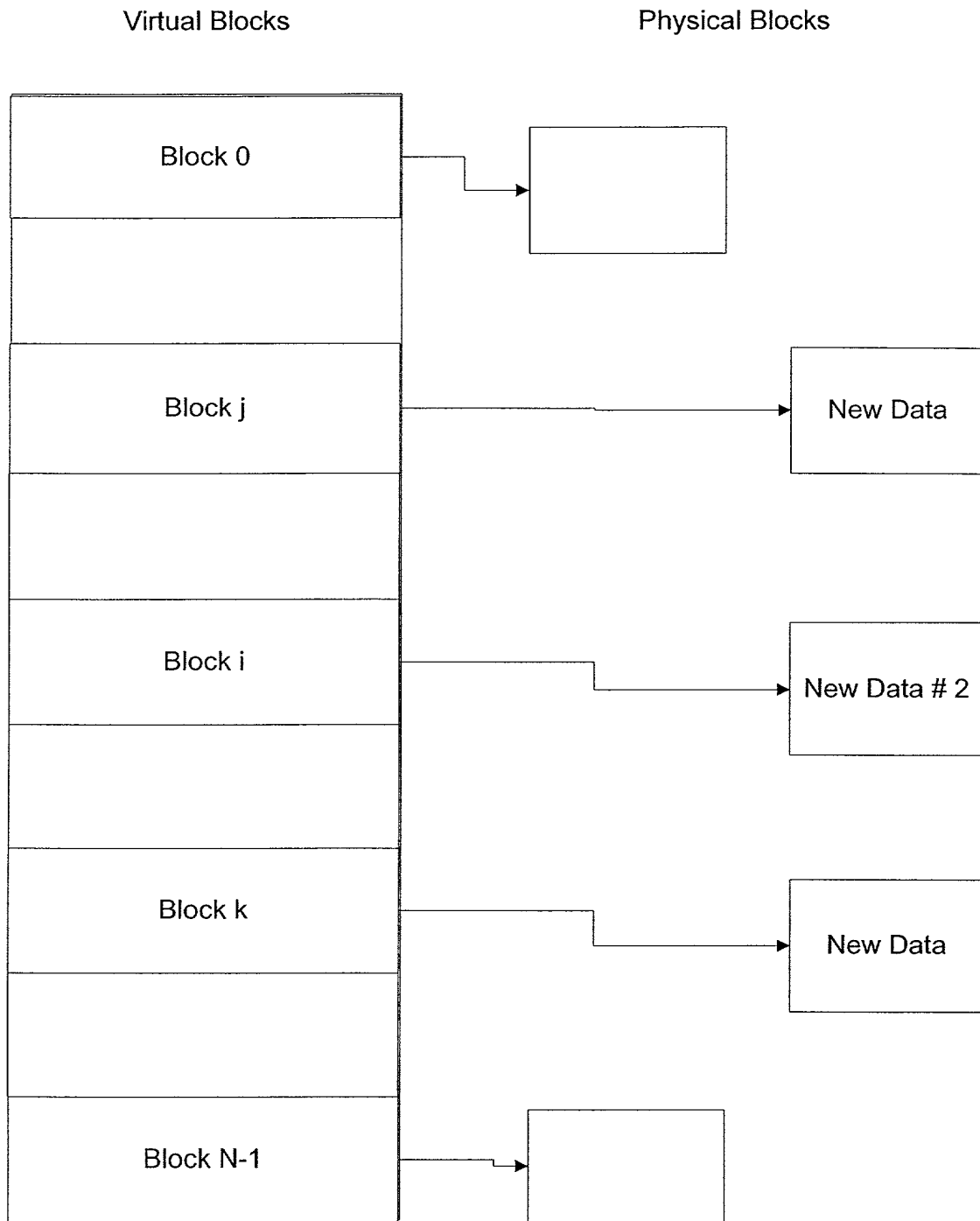


FIGURE 4E

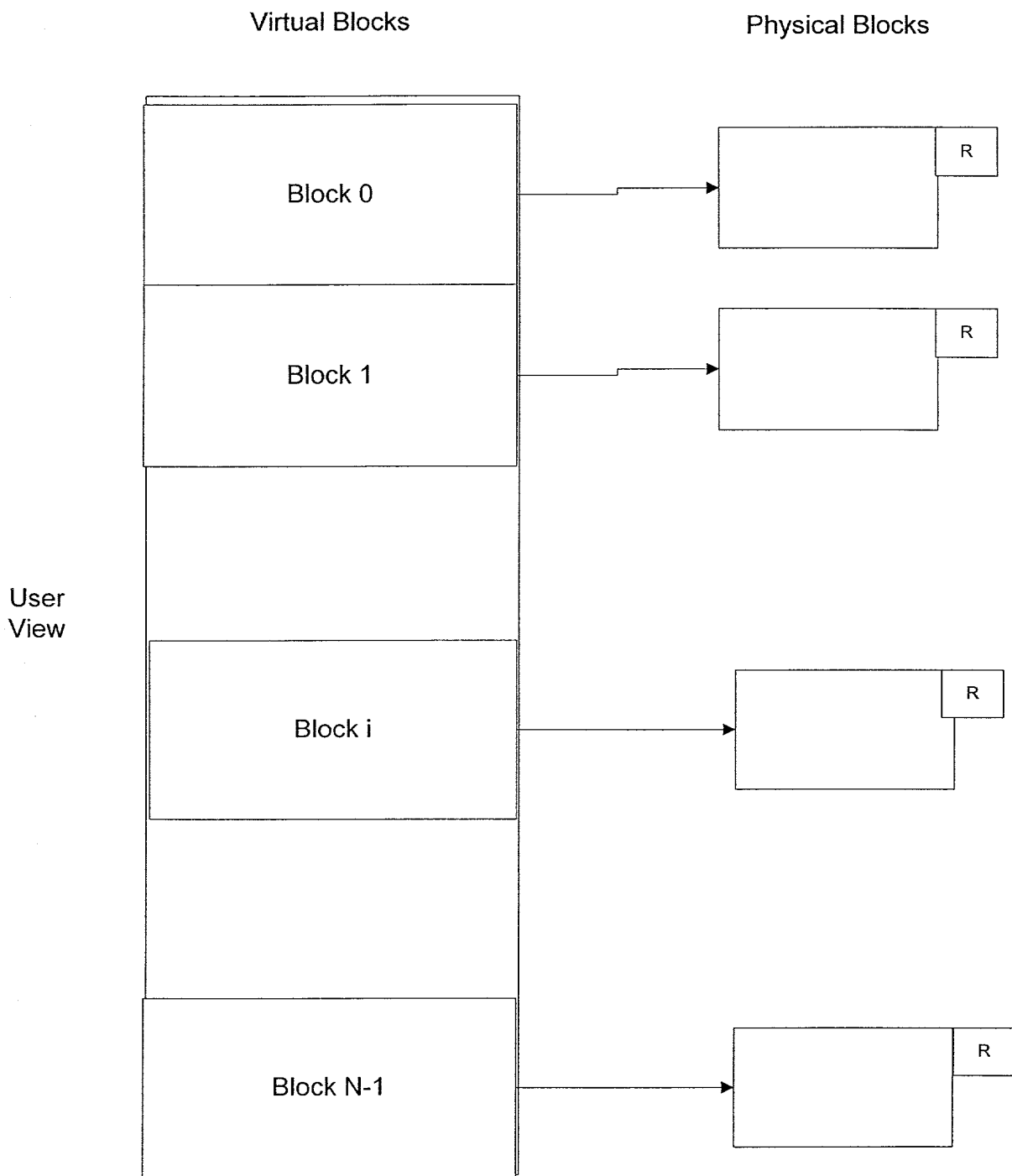


FIGURE 6A

User View

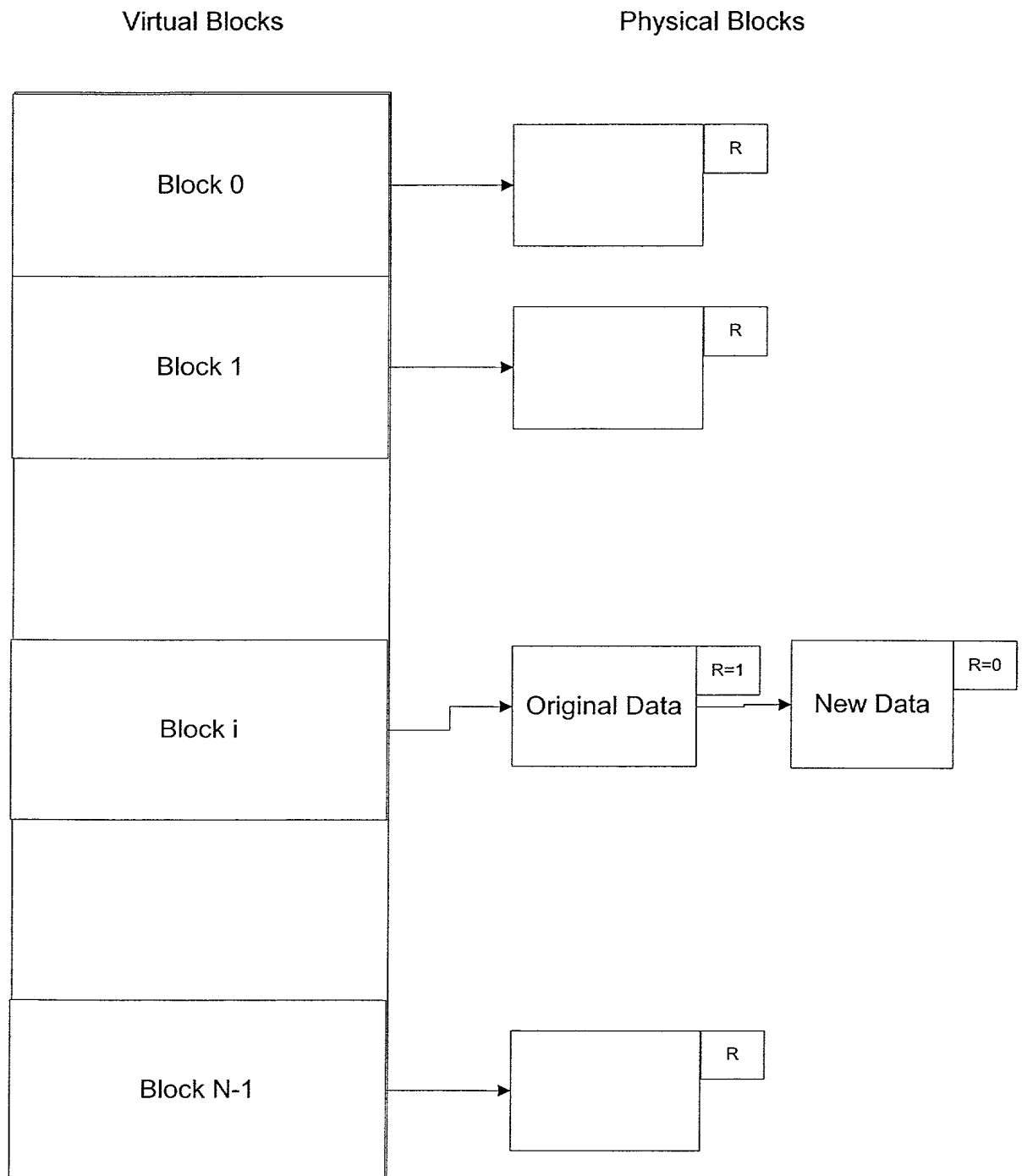


FIGURE 6B

FIGURE 6C

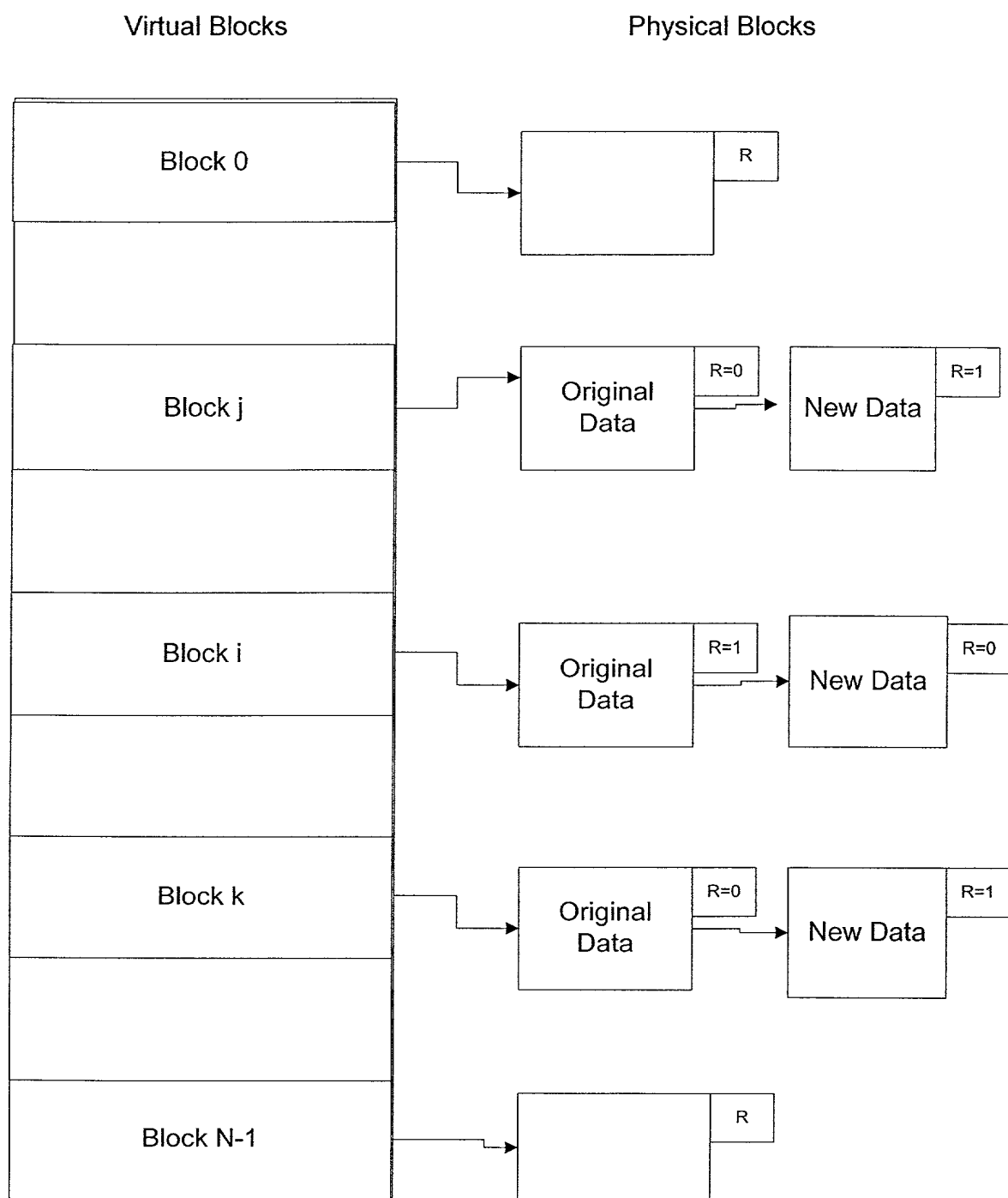


FIGURE 6C

User View

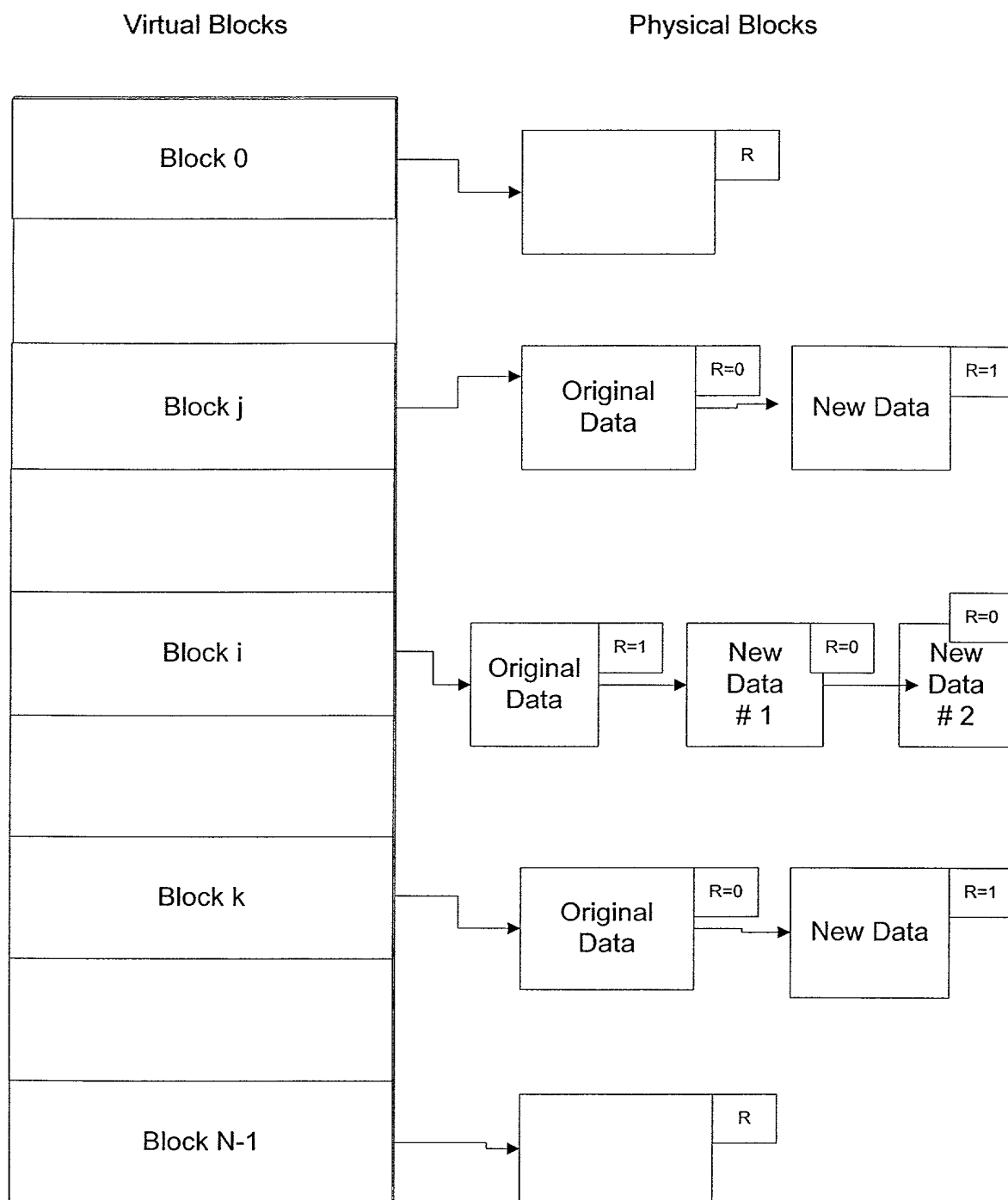


FIGURE 6D

Virtual Blocks

Physical Blocks

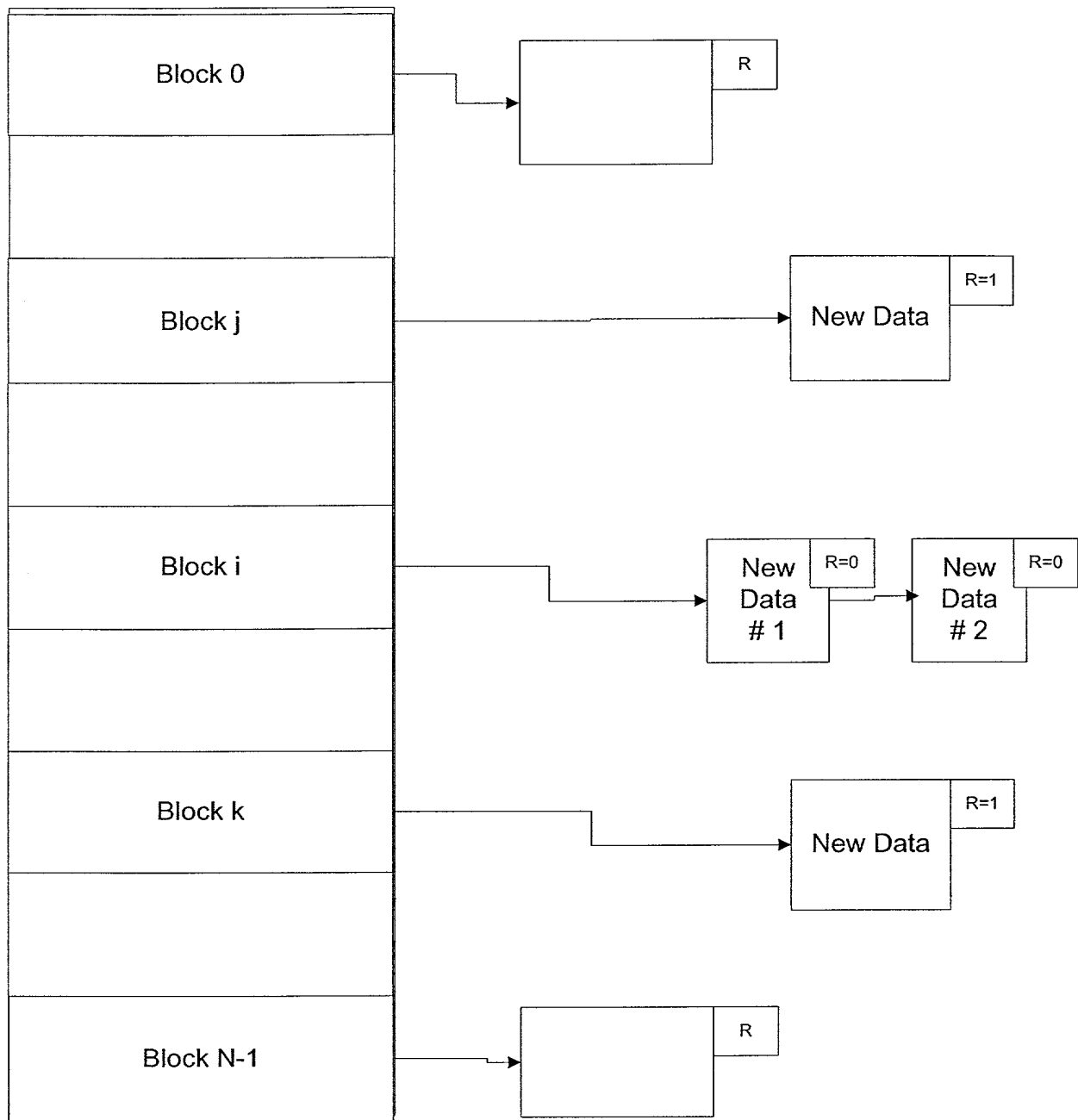


FIGURE 6E

Virtual Blocks

User View

Virtual Blocks

Physical Blocks

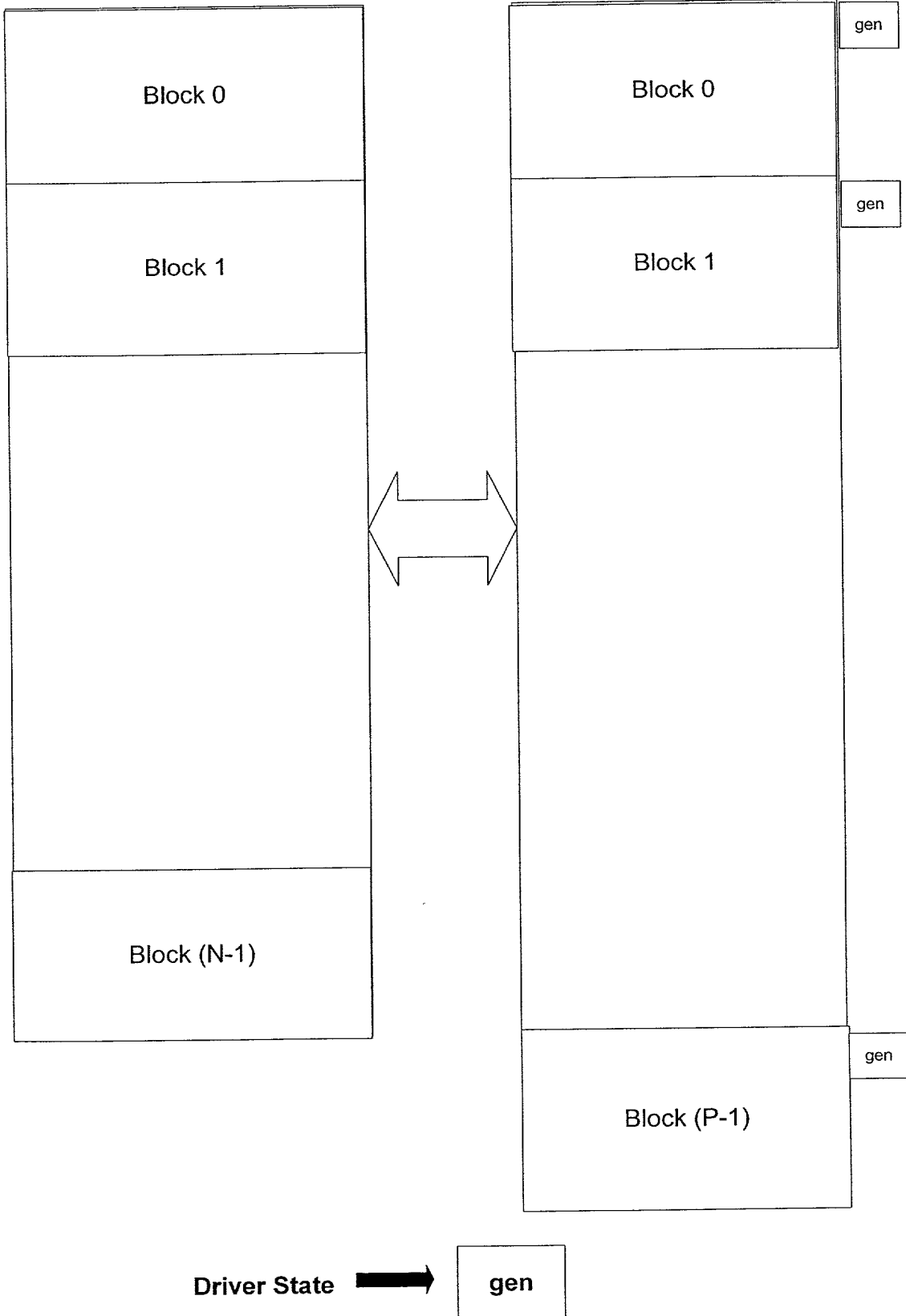


FIGURE 7

FIG. 8A

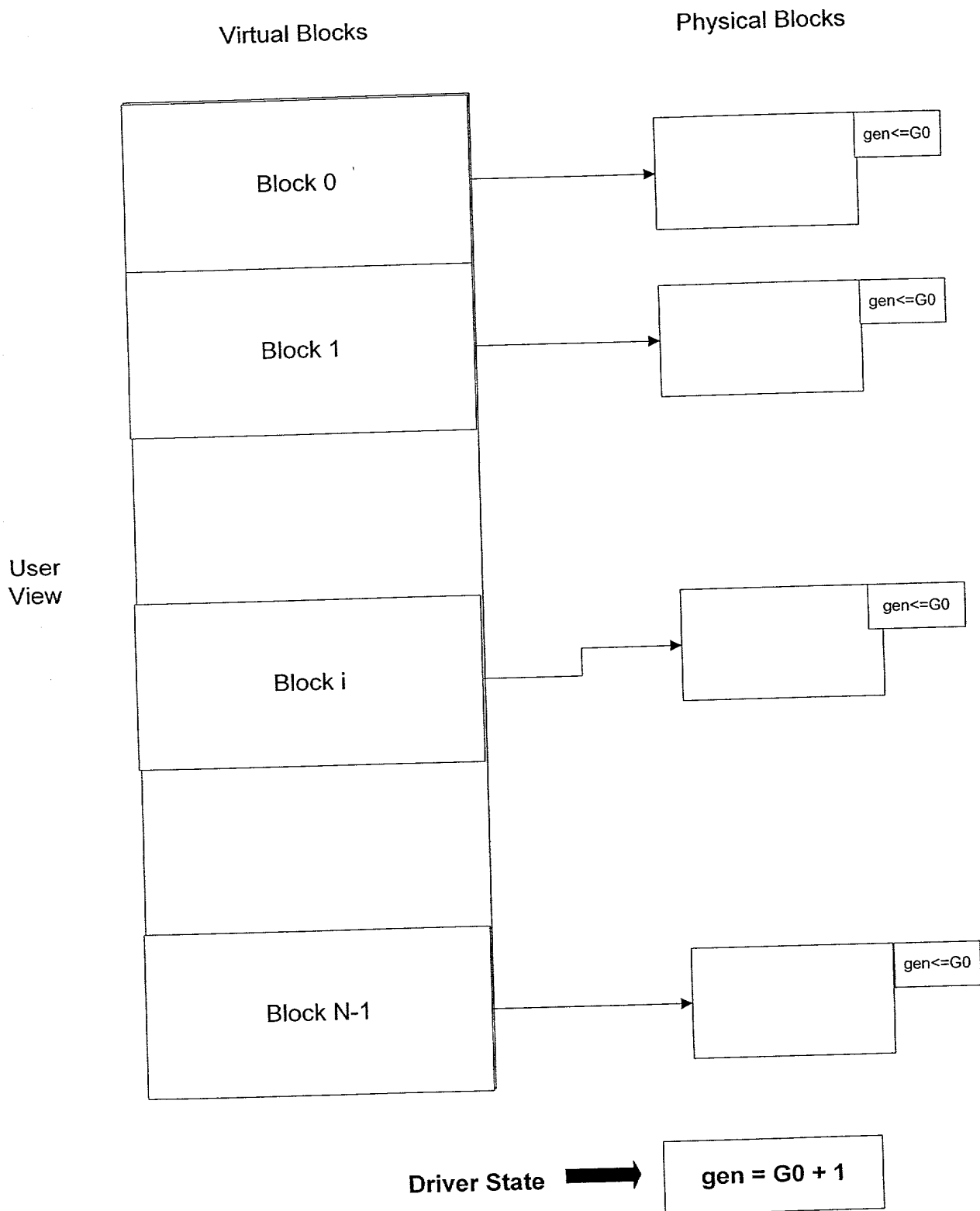


FIGURE 8A

Copyright © 2000 Intel Corporation

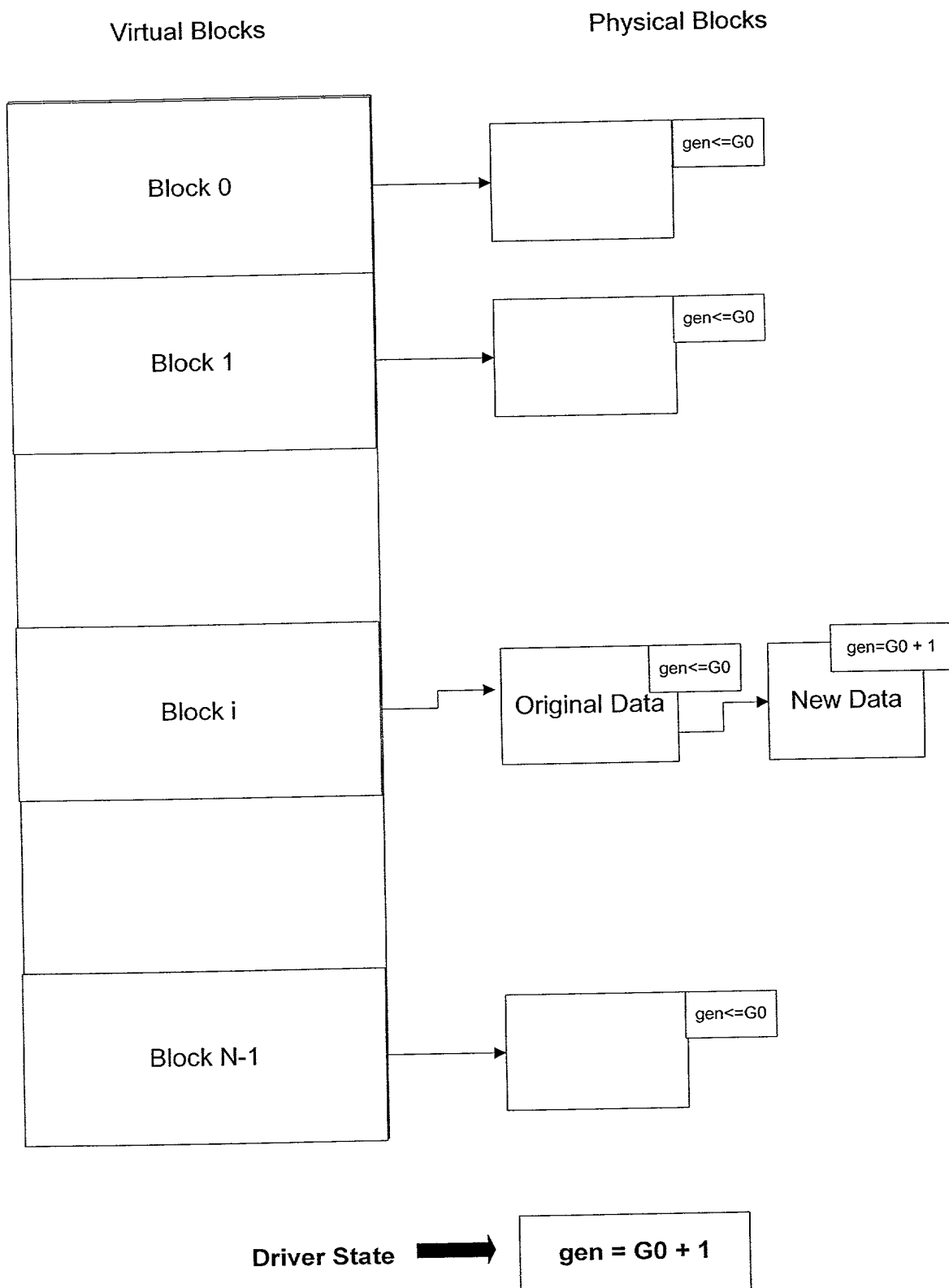


FIGURE 8B

User
View

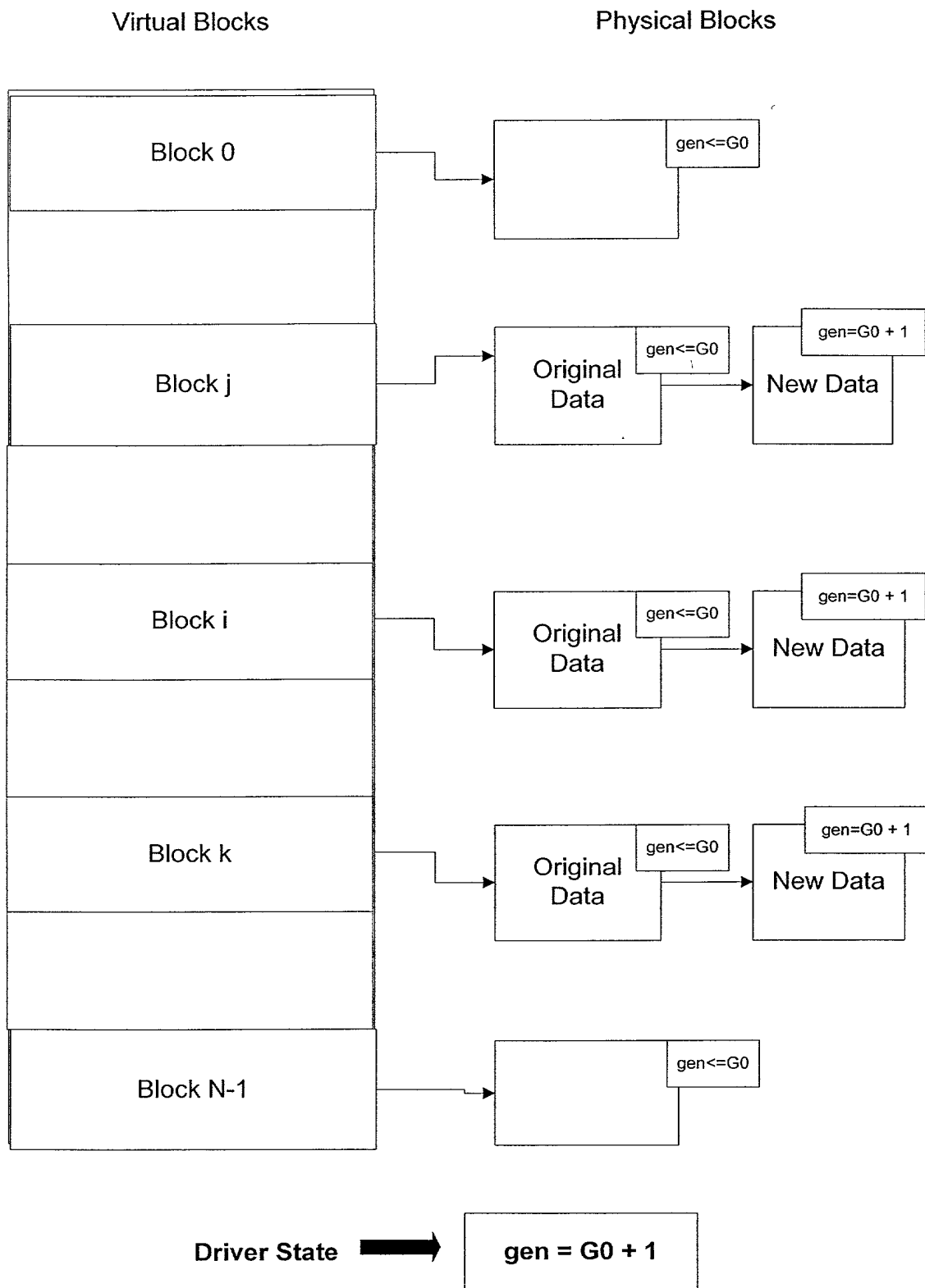


FIGURE 8C

User
View

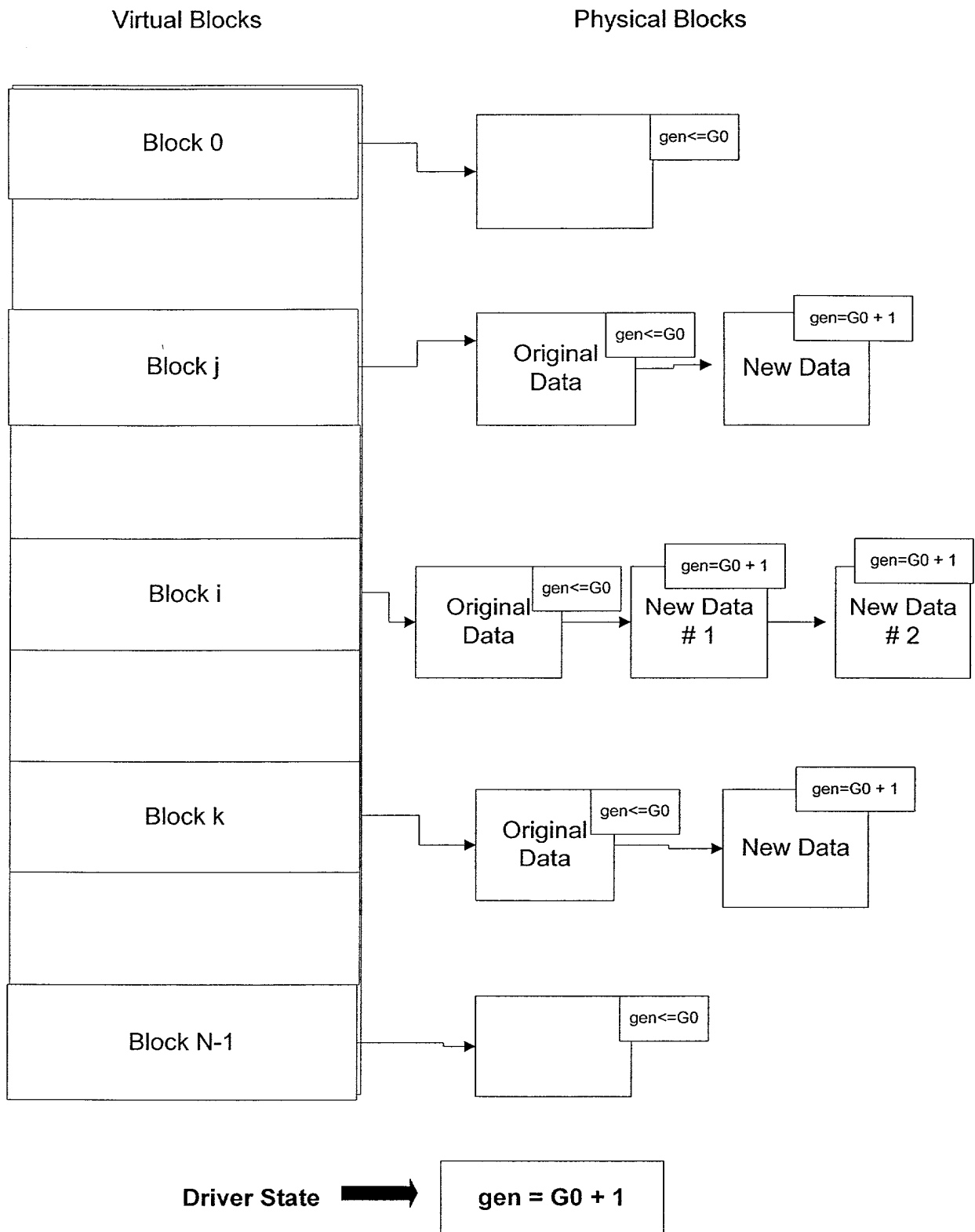


FIGURE 8D

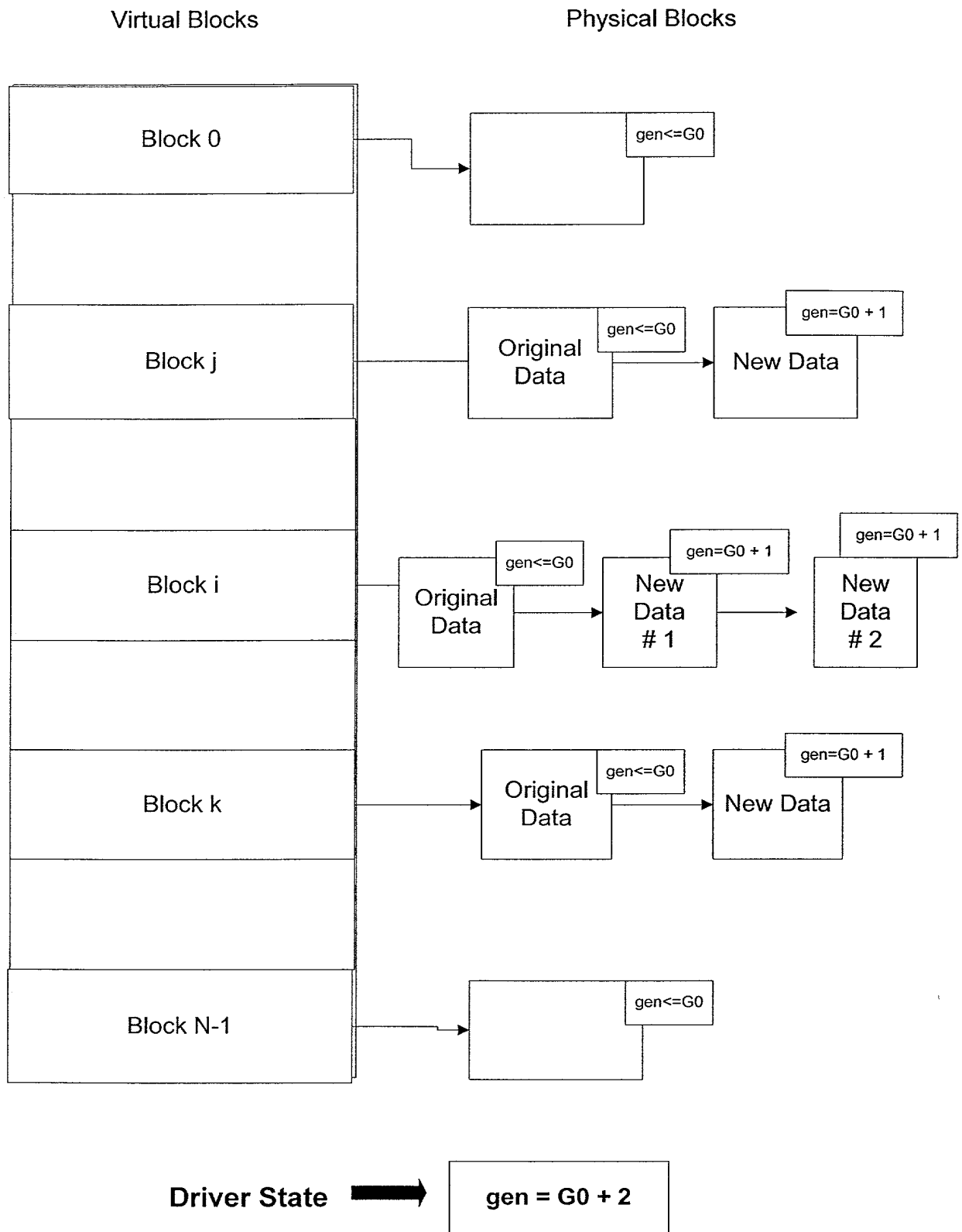


FIGURE 8E

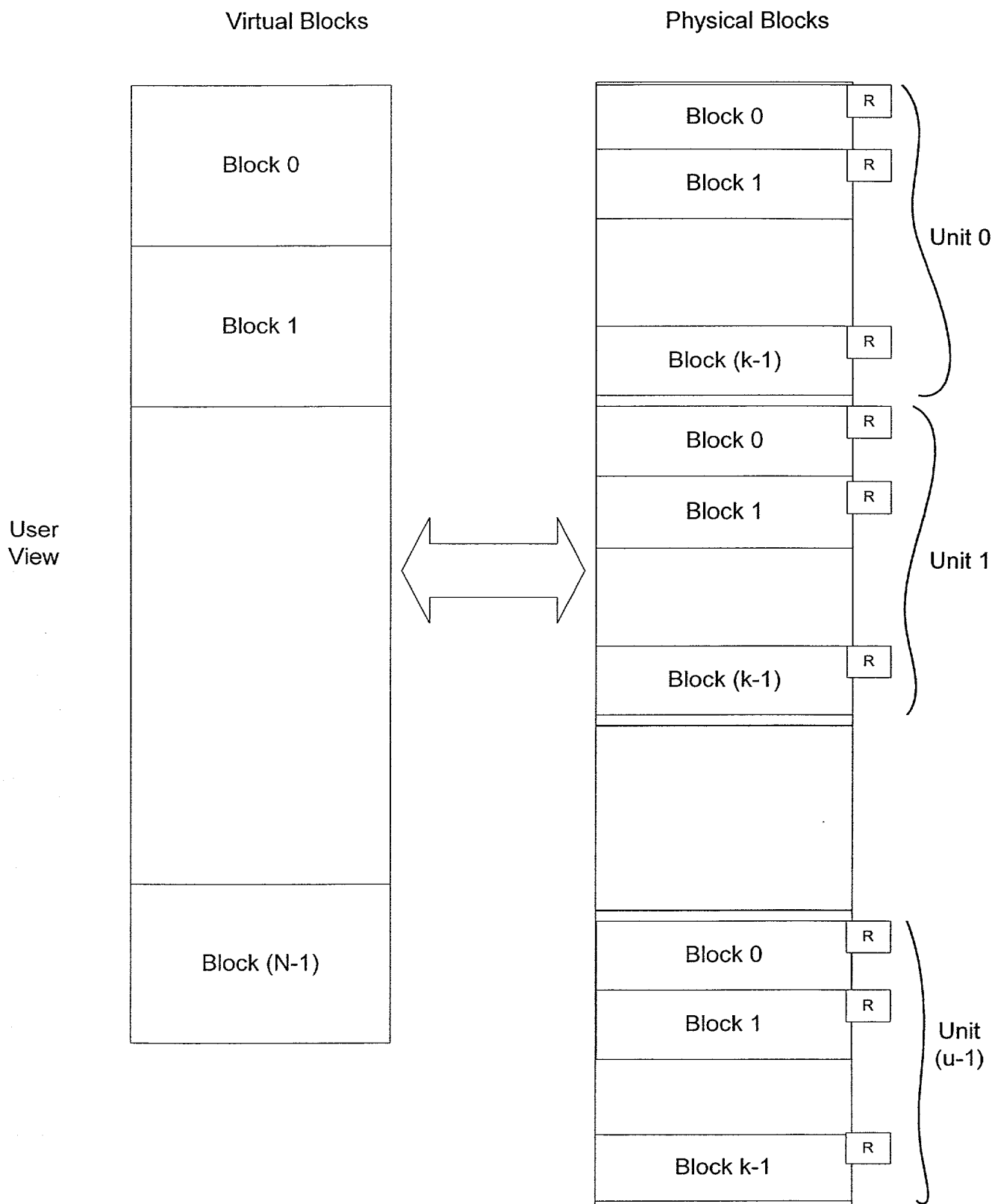


FIGURE 9

Physical Unit #1

Physical Unit #2

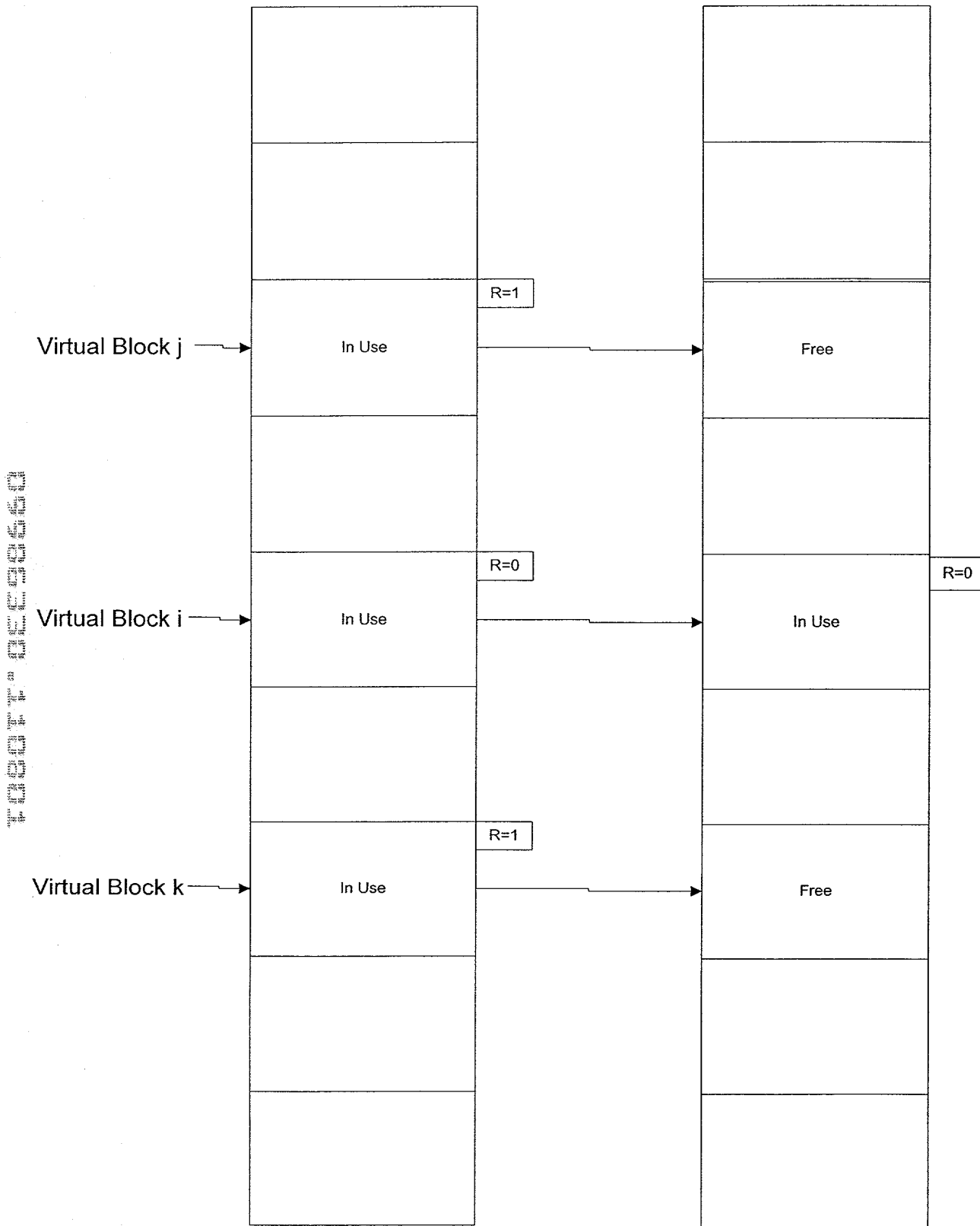


FIGURE 10A

FIGURE 10B

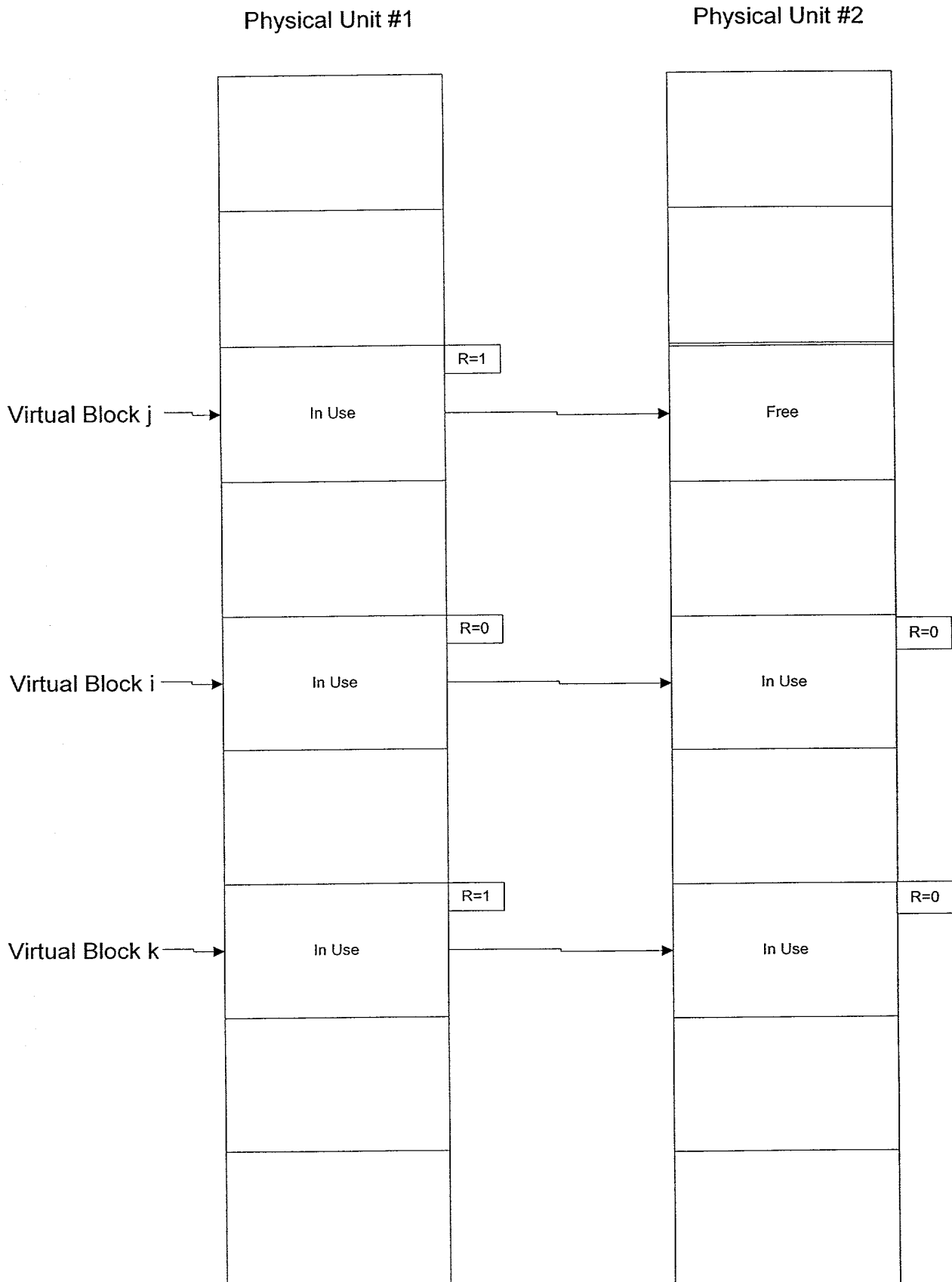


FIGURE 10B

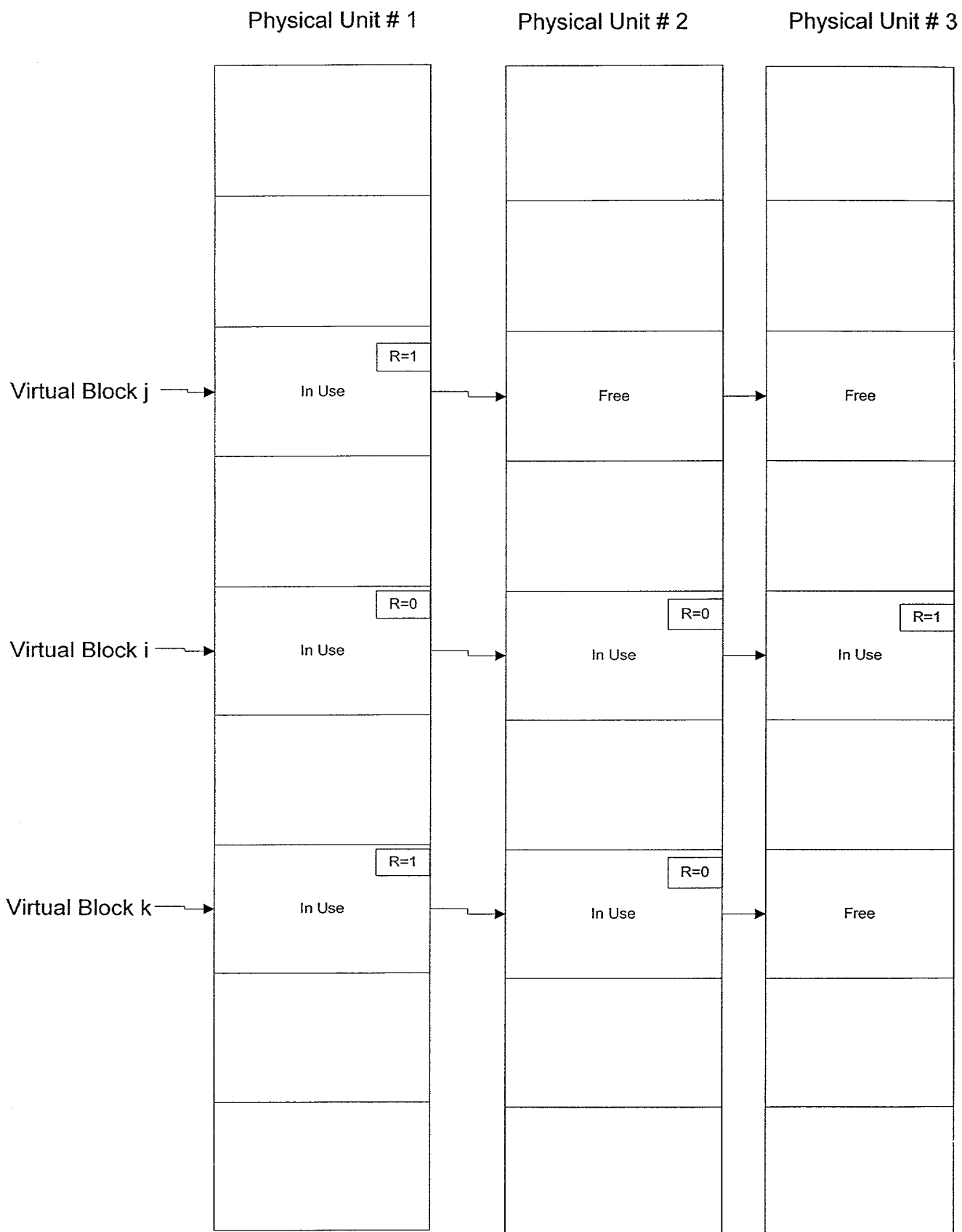


FIGURE 10C

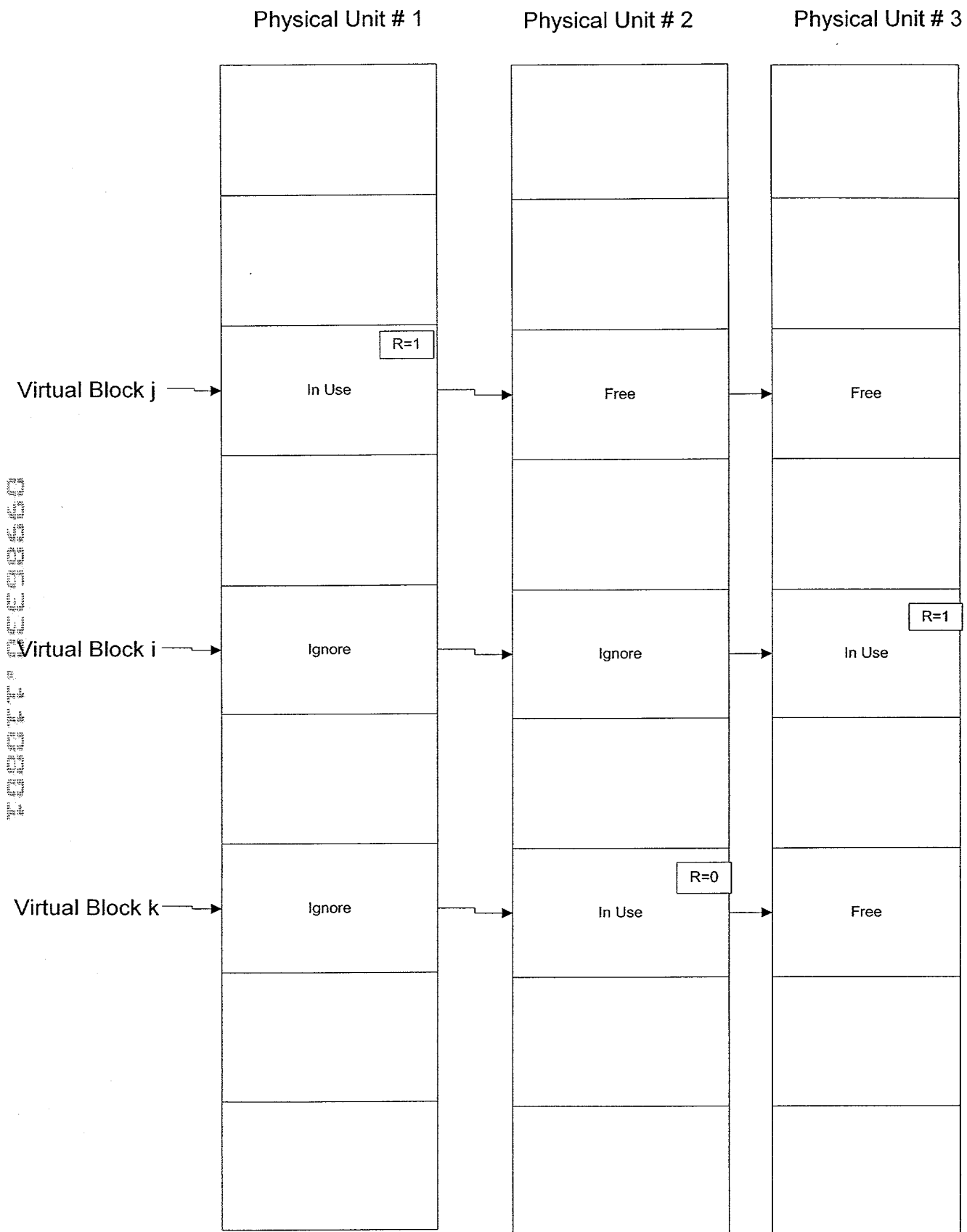


FIGURE 10D

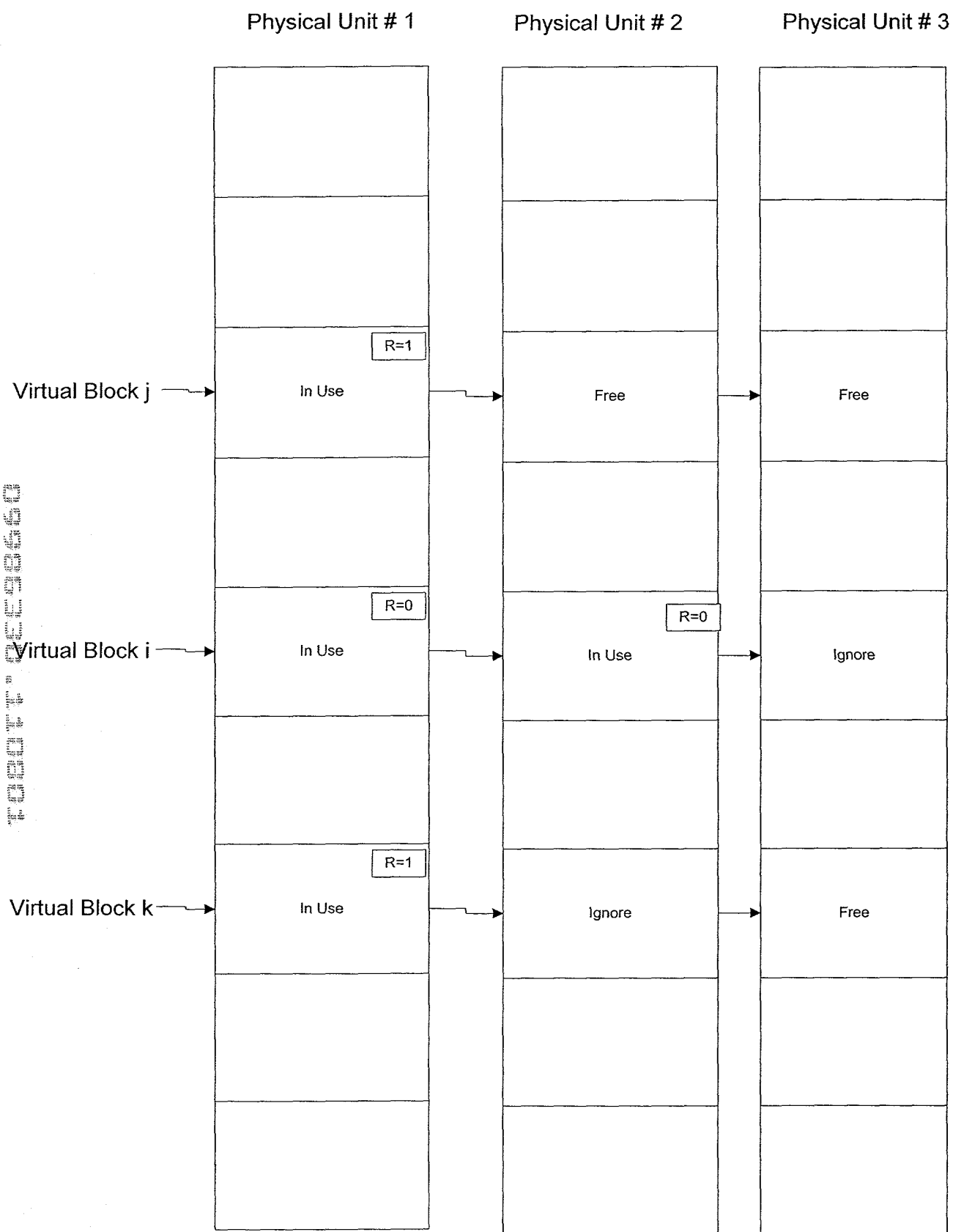


FIGURE 10E